

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
14 March 2002 (14.03.2002)

PCT

(10) International Publication Number
WO 02/21883 A1

(51) International Patent Classification⁷: H05B 33/12

(21) International Application Number: PCT/SG00/00134

(22) International Filing Date:
6 September 2000 (06.09.2000)

(25) Filing Language: English

(26) Publication Language: English

(71) Applicant (*for all designated States except US*): Osram
Opto Semiconductors GmbH & Co. OHG [DE/DE];
Wernerwerkstrasse 2, 93049 Regensburg (DE).

(72) Inventor; and

(75) Inventor/Applicant (*for US only*): GUENTHER, Ewald,
Karl, Michael [DE/SG]; 21 D Mount Sinai Rise, Singa-
pore 276938 (SG).

(74) Agent: EPPING HERMAN & FISCHER; Postfach 12
10 26, 80034 Munchen (DE).

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ,
DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR,
HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR,
LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ,
NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM,
TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

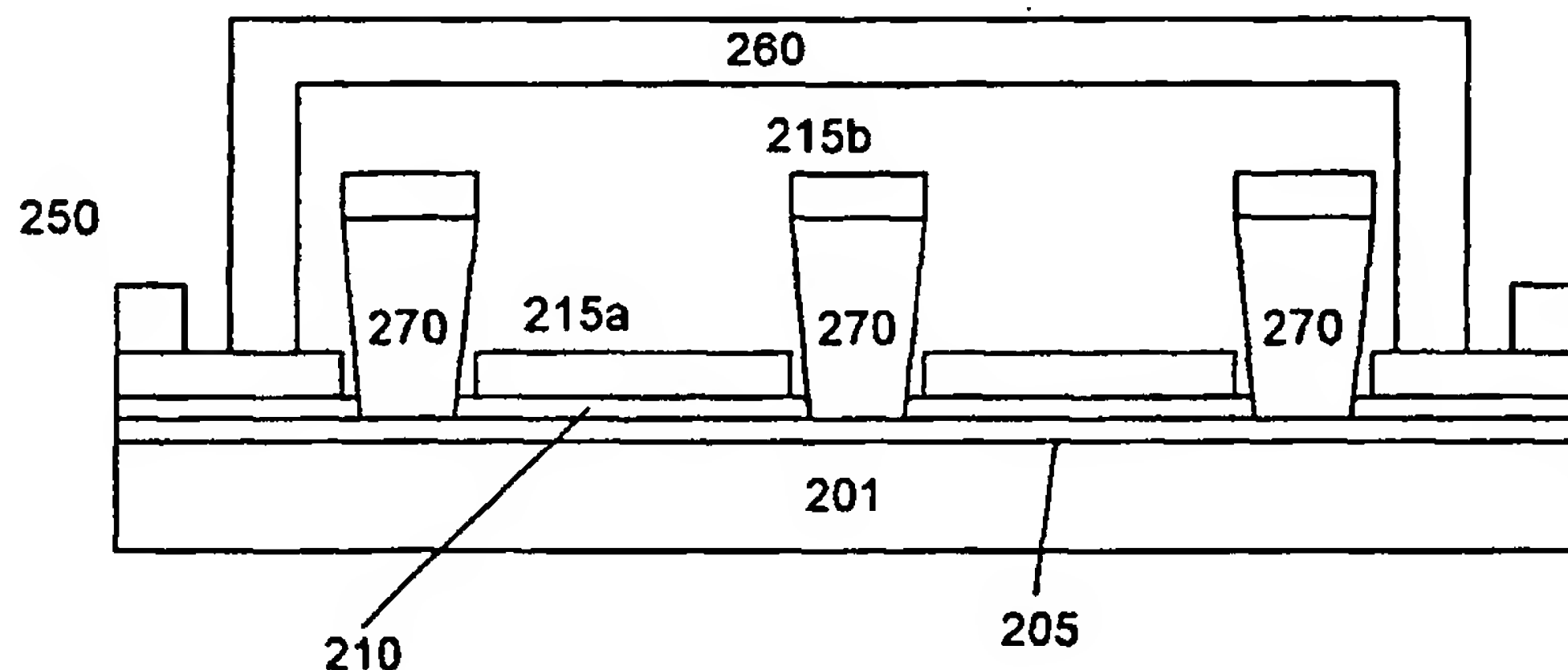
(84) Designated States (*regional*): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian
patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European
patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE,
IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG,
CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: PATTERNING OF ELECTRODES IN OLED DEVICES



(57) Abstract: An OLED device (200) having pillars (270) with cross section that is wider on the top. The pillars (270) structure a conductive layer (215) during deposition into distinct portions (215a, 215b) located between the pillars and on the top of the pillars. The pillars (270) are formed by patterning a single photosensitive material which is inert to the chemicals used to deposit the organic functional layers (210) of the OLED device (200). By using such pillars (270), the electrodes (215) can be formed without damaging organic functional layers (210) while achieving high

resolution.

WO 02/21883 A1

PATTERNING OF ELECTRODES IN OLED DEVICES

Field of the Invention

The present invention relates to organic light
5 emitting diode (OLED) devices. More particularly, the
invention relates to the patterning of a conductive
layer to form, for example, cathodes in OLED devices.

Background of the Invention

10 Fig. 1 shows a conventional pixelated OLED device
100. Pixelated OLED devices can be used as displays in
various consumer electronic products, including cellular
phones, cellular smart phones, personal organizers,
pagers, advertising panels, touch screen displays,
15 teleconferencing and multimedia products, virtual
reality products, and display kiosks.

Typically, the OLED device comprises a functional
stack of one or more organic functional layers 110
between a transparent conductive layer 105 and a
20 conductive layer 115. The functional stack is formed on
a transparent substrate 101. The conductive layers are
patterned to form rows of cathodes in a first direction
and columns of anodes in a second direction. OLED
pixels are located where the cathodes and anodes

overlap. Bond pads 150 are coupled to the cathodes and anodes to control the OLED pixels. A cap 160, which forms a cavity 145 between it and the pixels, encapsulates the device to protect the OLED pixels from
5 the environment such as moisture and/or air.

In operation, charge carriers are injected through the cathodes and anodes for recombination in the functional layers. The recombination of the charge carriers causes the functional layer of the pixels to
10 emit visible radiation.

To provide a display with high resolution and high filling factor, the spacing between pixels should be small, for example, about less than 50 um. The spacing between the pixels is defined by the patterning
15 processes that form the cathodes and anodes. Various conventional patterning techniques have been used to form the cathodes, such as shadow masking, photolithography (with wet or dry etching), laser ablation, or lift-off techniques (wet or dry resists).
20 However, conventional patterning techniques are not fully compatible or feasible for fabricating OLEDs. For example, photolithographic techniques employ chemicals which damage the organic functional layers or cathode materials. With shadow masking or lift-off techniques

(wet resists as well as dry resist foils), high resolutions (e.g., less than 50 um) are difficult to achieve, particularly in a manufacturing or production environment.

5 As evidenced from the above discussion, it is desirable to provide a patterning technique to pattern a conductive layer which achieves high resolutions without damaging already deposited materials.

10 Summary of the Invention

The invention relates generally to the fabrication of devices such as OLED devices. More particularly, the invention relates to the patterning of a conductive layer. In one embodiment, pillars with an undercut
15 (e.g., cross-section which is wider on top) are provided. In one embodiment, the pillars comprise a tapered profile. The pillars are formed by photolithography. In one embodiment, the pillars are formed from a photosensitive material. The pillars are
20 inert to the solvents used to deposit the functional organic layers. The tapered profile of the pillars patterns a conductive layer into distinct first and second portions during deposition. The first portions are located between the pillars and the second portions

are located on the top of the pillars. In one embodiment, the first portions serve as cathodes of an OLED device.

Brief Description of the Drawings

5 Fig. 1 shows a conventional OLED device;
 Fig. 2 shows an embodiment of the invention; and
 Figs. 3-7 show a process for fabricating an OLED device in accordance with one embodiment of the invention.

10

Preferred Embodiments of the Invention

 Fig. 2 shows an OLED device 200 in accordance with one embodiment of the invention. The OLED device comprises a substrate 201 on which pixels are formed.

15 In one embodiment, the substrate comprises a transparent substrate, such as glass. Other types of transparent materials that can serve as a substrate to support the OLED pixels are also useful. The OLED pixels comprise one or more organic layers 210 sandwiched between first

20 and second electrodes 205 and 215. In one embodiment, the first electrodes 205 are anodes and the second electrodes 215 are cathodes. Forming first electrodes that are cathodes and second electrodes that are anodes is also useful. The cathodes and anodes are formed as

strips in respective first and second directions. Typically, the first and second directions are orthogonal to each other. Bond pads 250 are electrically coupled to the cathodes and anodes. A cap 260 is provided to encapsulate the OLED pixels. The cap creates a cavity 245 to protect the pixels from being damaged by physical contact with the cap.

Pillars 270 are provided on the substrate surface. The pillars are arranged in a second direction. The height of the pillars extend above the functional stack (e.g., 1-10 μm), separating the cathodes and the organic functional layers into strips. In an alternative embodiment, the pillars can also extend the height of the cavity to serve as support structures for the cap 260. This is particularly useful for flexible OLED devices since the cap is prevented from contacting and damaging the pixels.

In accordance with the invention, the pillars include an undercut, which results in structures which are wider at the top than at the bottom. The undercut serves to pattern a conductive layer 215 during deposition to form distinct or separate portions 215a above the functional layers and portions 215b on the top of the pillars. The portions 215a serve as cathodes.

In one embodiment, the undercut is provided by forming pillars with a tapered profile. In one embodiment, the first and second sidewalls are tapered, producing pillars with a v-shaped cross-section.

5 In one embodiment, the pillars are formed from a single device layer. Forming the pillars from a single layer advantageously reduces manufacturing cost. Since the chemicals, such as solvents for depositing the functional organic layer or layers can be corrosive, the
10 pillars are formed from a material that maintains its integrity during processing. In one embodiment, the pillars are formed using a photosensitive material such as resist. The resist is treated to render it inert to solvents. Other types of photosensitive materials, such
15 as photosensitive polyimide or photosensitive polybenzosazole, are also useful. In addition, electron-cure resist system, such as those manufactured by Allied Signal, can also be used to form pillars having the desired cross-sectional shape.

20 Figs. 3-7 show a process for fabricating an OLED device in accordance with one embodiment of the invention. Referring to Fig. 3, a substrate 301 is provided. In one embodiment, the substrate comprises a transparent substrate, for example, soda lime or

borosilicate glass. Other types of transparent materials can also be used to serve as the substrate. The substrate typically is about 0.4 - 1.1 mm thick.

In another embodiment, the substrate comprises a
5 thin flexible substrate. Thin flexible substrates are formed from, for example, plastic films such as transparent poly(ethylene terephthalate) (PET), poly(butylene terephthalate) (PBT), poly(ethylene naphthalate) (PEN), polycarbonate (PC), polyimides (PI),
10 polysulfones (PSO), and poly(p-phenylene ether sulfone) (PES). Other materials such as polyethylene (PE), polypropylene (PP), poly(vinyl chloride) (PVC), polystyrene (PS) and poly(methyl methacrylate) (PMMA), can also be used to form the substrate.
15 Alternatively, materials such as ultra thin glass (e.g., thickness between 10-100 μm), a composite stack comprising glass and polymer or polymer films coated with inorganic barrier layers can also be used.

The substrate includes first electrodes 305 formed
20 on the surface thereof. The first electrodes serve as, for example, anodes. The anodes are formed from a conductive material. In one embodiment, the conductive material comprises a transparent conductive material such as indium-tin-oxide (ITO). Other transparent

conductive materials, for example, indium-zinc-oxide, zinc-oxide, tin-oxide, are also useful. In one embodiment, the anodes are arranged in strips in a first direction, each being separated by a space. Preferably, 5 the space separating the anodes is less than 50 um wide. Connections to bond pads can also be provided. Various techniques, such as photolithography, can be used to form the anodes.

Referring to Fig. 4, a device layer 469 is 10 deposited on the substrate. The device layer is used to create the pillars which facilitate patterning of a conductive layer to form the second electrodes (e.g., cathodes). The thickness of the device layer is equal to the height of the pillars. The thickness of the 15 device layer, for example, is about 0.5-50 um, preferably about 1-10 um.

In one embodiment, the device layer comprises a negative acting photoresist such as AZ 5214E manufactured by Clariant. Other photosensitive 20 materials can also be used. The resist is deposited on the substrate by, for example, spin-coating. In one embodiment, the resist is deposited by spinning the substrate at 1000 rpm for about 20 seconds using a Karl Suess RC 8 spin-coater. After depositing the resist,

the substrate is baked at, for example, 90°C for about 2 minutes to remove the resist solvent.

In Fig. 5, the device layer is selectively exposed to light from an exposure source through a mask 520.

5 The exposure process is designed to form pillars with the desired undercut or tapered profile. The exposure process comprises, for example, successively exposing the photosensitive layer with electrons or charged particles having different energies which have different
10 penetration depths to form the tapered profile during development. For a negative photosensitive layer, the exposure comprises selectively exposing the upper regions of the device layer with a lesser amount of energy than the lower regions. For a positive
15 photosensitive layer, the exposure comprises selectively exposing the upper regions of the device layer with a greater amount of energy than the lower regions.

In one embodiment, a negative resist layer is selectively exposed with an exposure source. The
20 exposure results in the upper portions of regions 566 absorbing a greater amount of light than the lower portions (i.e., underexposing the lower portions of regions 566). Regions 566 correspond to locations where pillars are to be formed. In one embodiment, the resist

is exposed with I line radiation using a Karl Sues MJB 3 exposure source. The exposure dose is about 50 mJ/cm².

The resist is then prepared for development to remove the unexposed portions. The preparation, for a negative acting resist, includes a post-exposure bake to cross-link the resist in the exposed regions. The post-exposure bake is performed at, for example, about 120°C for about 60-90 seconds. Cross-linking renders the resist insoluble to the resist development chemistry. After the post-exposure bake, the resist is subjected to a flood exposure from the exposure source (e.g. exposure without mask). The flood exposure renders the previously un-exposed portions of the resist soluble. The dose of the flood exposure is, for example, about 1000 mJ/cm².

Referring to Fig. 6, the device layer is developed with a resist development chemistry to remove the unexposed regions, leaving pillars 670. In one embodiment, the resist chemistry comprises an alkaline developer such as A2 726 manufactured by Clariant. The resist is developed in the development chemistry at room temperature for about 60 seconds. Because the lower parts of the exposed regions were underexposed, they are more soluble to the resist chemistry. This creates

pillars with an undercut, resulting in a cross-section that is narrower at the bottom than the top. The resist is then rinsed with de-ionized water to remove the developer.

5 After forming the pillars, the resist is cured to improve the mechanical stability of the pillars and to render pillars inert to the organic solvents used to form the functional organic layers. In one embodiment, the resist is cured by heating the substrate at a
10 temperature of about 160°C for about 6 hours. In one embodiment, the substrate is cured in accordance with the following parameters:

- a) linear ramp from 100°C to 160°C in 2 hours;
- b) constant curing at 160°C for 4 hours; and
- 15 c) cool down without active cooling.

Other curing techniques such as electrom beam (e-beam), particle (proton, alpha) or UV curing can also be used. After curing, the substrate is cleared by subjecting it to UV-O₃ for about 3 minutes, removing small organic
20 residues on the exposed portions of the substrate.

Referring to Fig. 7, a functional organic layer is deposited on the substrate. In one embodiment, the functional organic layer comprises a conjugated polymer. The polymer is dissolved in a solvent and deposited by

spin-coating techniques. In one embodiment, the organic functional layer comprises a 1 % solution of electroluminescent polymer dissolved in xylene deposited by spinning the substrate at 4000 rpm for about 30 seconds.

5 Other wet deposition techniques are also useful. Such techniques, for example, include printing techniques (e.g., screen printing, off-set printing, ink-jet printing) in which the organic functional layer is dissolved in a solvent (e.g., NMP, or hexene).

10 Depositing the organic functional layer by a wet process is advantageous as it is substantially self planarizing, resulting in the layer filling the area between the pillars with a substantially planar surface. The pillars, due to curing, are not adversely affected by

15 the solvents. Additional functional layers can be deposited to form a functional organic stack. The thickness of the organic layer or stack is typically about 2 - 200 nm. After depositing the functional organic layer, the substrate is heated to a temperature

20 of about 85°C for about 1 minute to evaporate the solvent.

Portions of the organic layer can be selectively removed, for example, to expose underlying layers in regions 470 for bond pad connections. Selective removal

of the organic layers can be achieved by a polishing process. Other techniques, such as etching, scratching, or laser ablation, can also be used to selectively remove portions of the organic layers.

5 A conductive layer 715 is deposited on the substrate. The conductive layer comprises, for example, Ca, Mg, Ba, Ag, Al or a mixture or alloy thereof. Other conductive materials, particularly those comprising a low work function, can also be used to form the second
10 conductive layer. In one embodiment, the second conductive layer comprises Ca. The Ca is deposited by thermal evaporation at a rate of 1 nm/s and a pressure of about 10^{-5} mbar. Other deposition techniques, such as sputtering (PVD), chemical vapor deposition (CVD),
15 plasma enhanced chemical vapor deposition (PECVD) or metal organic chemical vapor deposition (MOCVD), are also useful.

 The deposition of the conductive layer is interrupted due to the height and profile of the
20 pillars, resulting in parts 715a of the conductive layer being deposited in regions between the pillars and parts 715b of the conductive layer being formed on the top surface of the pillars. The parts of the conductive layer between the pillars serve as cathodes. The

intersections of the cathodes and anodes form organic LED pixels.

The process continues to complete the OLED device. For example, a cavity cap is mounted on the substrate to
5 encapsulate the device and bond pads are formed to provide electrical access to the OLED pixels.

While the invention has been particularly shown and described with reference to various embodiments, it will be recognized by those skilled in the art that
10 modifications and changes may be made to the present invention without departing from the spirit and scope thereof. The scope of the invention should therefore be determined not with reference to the above description but with reference to the appended claims along with
15 their full scope of equivalents.

What is claimed is:

1. A method for forming an OLED device comprising:
forming device layer on a substrate;
patterning the device layer to form pillars along a
5 first direction on the substrate, wherein the pillars
comprises a tapered profile;
coating the substrate with a solution comprising an
organic functional material dissolved in a solvent, the
pillars being inert to the solvent;
10 removing the solvent to form an organic functional
layer; and
depositing a conductive layer on the substrate,
wherein the tapered profile of the pillars separate the
conductive layer into first and second distinct
15 portions.
2. The method of claim 1 further comprises mounting a
cap on the substrate to hermetically seal the OLED
device.
- 20 3. The method of claim 1 wherein the substrate
comprises a flexible substrate.

4. The method of claim 3 wherein the substrate comprises electrodes in a second direction on a surface thereof.

5 5. The method of claim 4 further comprises mounting a cap on the substrate to hermetically seal the OLED device.

6. The method of claim 3 wherein the functional
10 organic material comprises a conjugated polymer dissolved in a solvent.

7. The method of claim 6 further comprises mounting a cap on the substrate to hermetically seal the OLED
15 device.

8. The method of claim 3 further comprises mounting a cap on the substrate to hermetically seal the OLED device.

20

9. The method of claim 1 wherein the substrate comprises electrodes in a second direction on a surface thereof.

10. The method of claim 9 wherein the functional organic material comprises a conjugated polymer dissolved in a solvent.

5 11. The method of claim 10 further comprises mounting a cap on the substrate to hermetically seal the OLED device.

12. The method of claim 12 further comprises mounting a
10 cap on the substrate to hermetically seal the OLED device.

13. The method of claim 1 further comprises mounting a cap on the substrate to hermetically seal the OLED
15 device.

14. The method of claim 13 further comprises mounting a cap on the substrate to hermetically seal the OLED device.

20

15. The method as recited in claim 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, or 14 wherein the device layer comprises a photosensitive device layer, the

photosensitive layer is patterned by exposing and developing the photosensitive device layer.

16. The method as recited in claim 15 wherein the
5 photosensitive layer comprises a positive photosensitive layer, wherein exposed portions of the photosensitive layer are removed during developing.

17. The method as recited in claim 16 wherein exposing
10 comprises successively exposing the photosensitive layer with electrons or charged particles having different energies which have different penetration depths to form pillars with the tapered profile during developing.

15 18. The method as recited in claim 17 comprises curing the pillars to render the pillars inert against the solvent.

19. The method as recited in claim 18 wherein the
20 curing comprises thermal curing.

20. The method as recited in claim 18 wherein the curing comprises UV curing.

21. The method as recited in claim 18 wherein the curing comprises electron beam curing.

22. The method as recited in claim 18 wherein the
5 curing comprises particle curing.

23. The method as recited in claim 16 wherein exposing comprises exposing upper regions of the photosensitive layer with a lesser amount of energy than lower regions
10 of the photosensitive layer to form the tapered profile during developing.

24. The method as recited in claim 23 comprises curing the pillars to render the pillars inert against the
15 solvent.

25. The method as recited in claim 24 wherein the curing comprises thermal curing.

20 26. The method as recited in claim 24 wherein the curing comprises UV curing.

27. The method as recited in claim 24 wherein the curing comprises electron beam curing.

28. The method as recited in claim 24 wherein the curing comprises particle curing.

5 29. The method as recited in claim comprises 16 curing the pillars to render the pillars inert against the solvent.

30. The method as recited in claim 29 wherein the
10 curing comprises thermal curing.

31. The method as recited in claim 29 wherein the curing comprises UV curing.

15 32. The method as recited in claim 29 wherein the curing comprises electron beam curing.

33. The method as recited in claim 29 wherein the curing comprises particle curing.

34. The method as recited in claim 15 wherein exposing comprises successively exposing the photosensitive layer with electrons or charged particles having different

energies which have different penetration depths to form pillars with the tapered profile during developing.

35. The method as recited in claim 34 comprises curing
5 the pillars to render the pillars inert against the solvent.

36. The method as recited in claim 35 wherein the curing comprises thermal curing.

10

37. The method as recited in claim 35 wherein the curing comprises UV curing.

38. The method as recited in claim 35 wherein the
15 curing comprises electron beam curing.

39. The method as recited in claim 35 wherein the curing comprises particle curing.

20 40. The method as recited in claim 15 comprises curing the pillars to render the pillars inert against the solvent.

41. The method as recited in claim 40 wherein the curing comprises thermal curing.

42. The method as recited in claim 40 wherein the
5 curing comprises UV curing.

43. The method as recited in claim 40 wherein the curing comprises electron beam curing.

10 44. The method as recited in claim 40 wherein the curing comprises particle curing.

45. The method as recited in claim 15 wherein the photosensitive layer comprises a negative photosensitive
15 layer, wherein unexposed portions of the photosensitive layer are removed during developing.

46. The method as recited in claim 45 wherein exposing comprises successively exposing the photosensitive layer
20 with electrons or charged particles having different energies which have different penetration depths to form pillars with the tapered profile during developing.

47. The method as recited in claim 46 comprises curing the pillars to render the pillars inert against the solvent.

5 48. The method as recited in claim 47 wherein the curing comprises thermal curing.

49. The method as recited in claim 47 wherein the curing comprises UV curing.

10

50. The method as recited in claim 47 wherein the curing comprises electron beam curing.

51. The method as recited in claim 47 wherein the
15 curing comprises particle curing.

52. The method as recited in claim 45 wherein exposing comprises exposing upper regions of the photosensitive layer with a greater amount of energy than lower regions
20 of the photosensitive layer to form the tapered profile during developing.

53. The method as recited in claim 52 comprises curing the pillars to render the pillars inert against the solvent.

5 54. The method as recited in claim 53 wherein the curing comprises thermal curing.

55. The method as recited in claim 53 wherein the curing comprises UV curing.

10

56. The method as recited in claim 53 wherein the curing comprises electron beam curing.

57. The method as recited in claim 53 wherein the
15 curing comprises particle curing.

58. The method as recited in claim 45 comprises curing the pillars to render the pillars inert against the solvent.

20

59. The method as recited in claim 57 wherein the curing comprises thermal curing.

60. The method as recited in claim 57 wherein the curing comprises UV curing.

61. The method as recited in claim 57 wherein the
5 curing comprises electron beam curing.

62. The method as recited in claim 57 wherein the curing comprises particle curing.

1 of 5

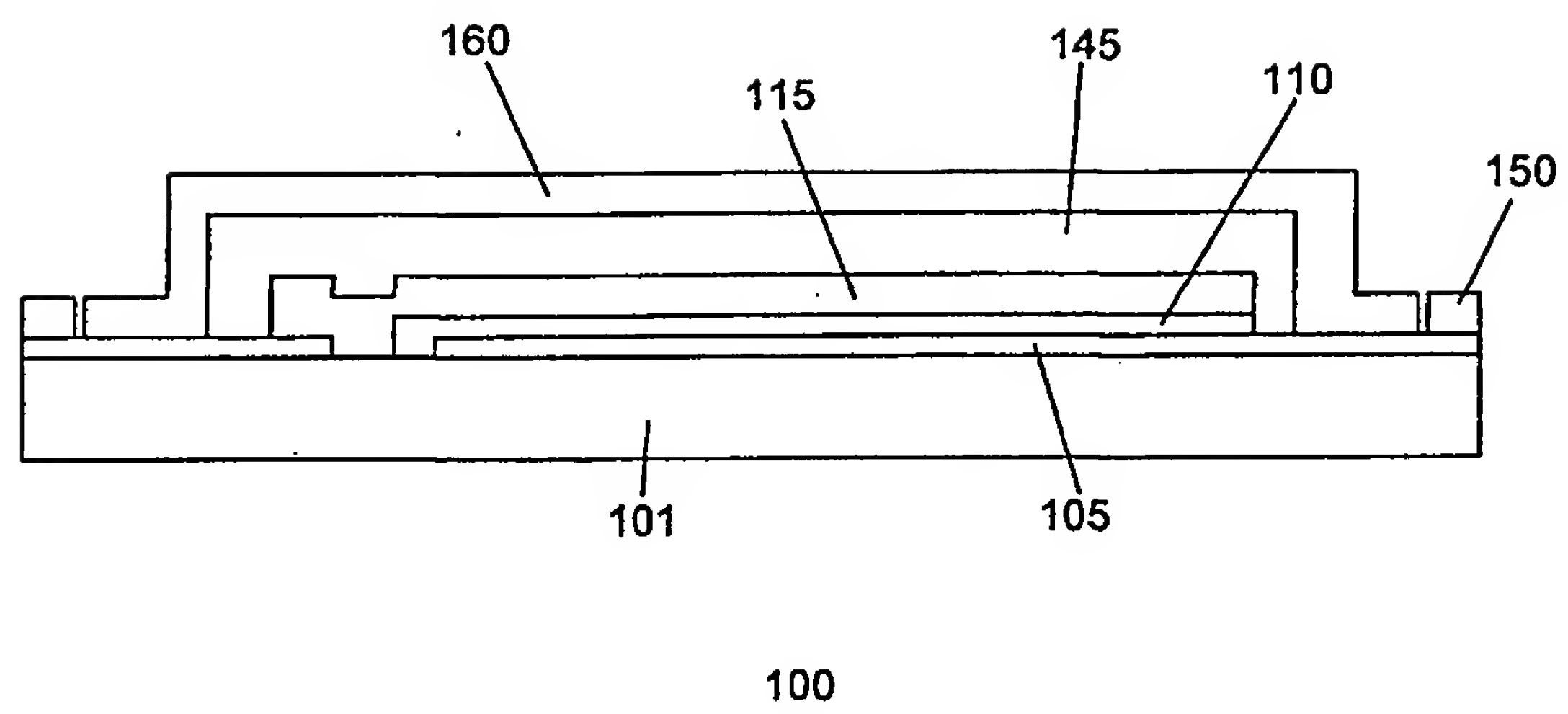


Fig. 1

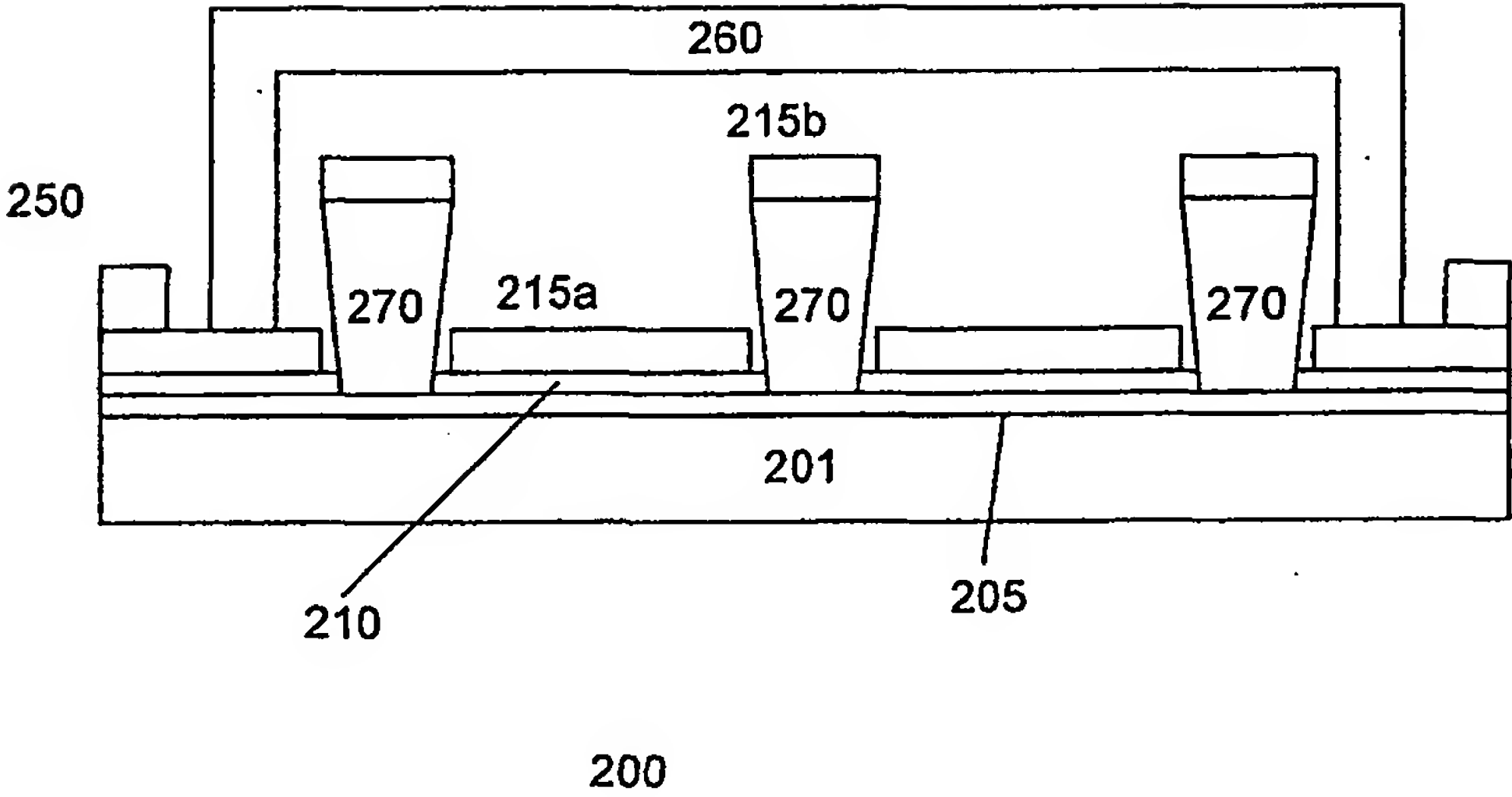


Fig. 2

3 of 5

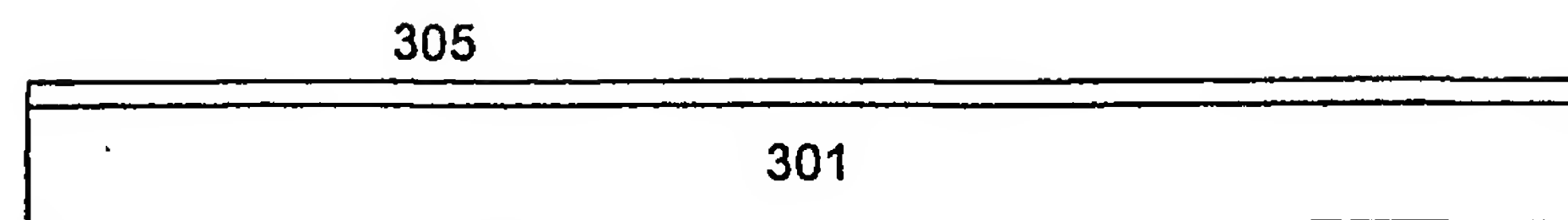


Fig. 3

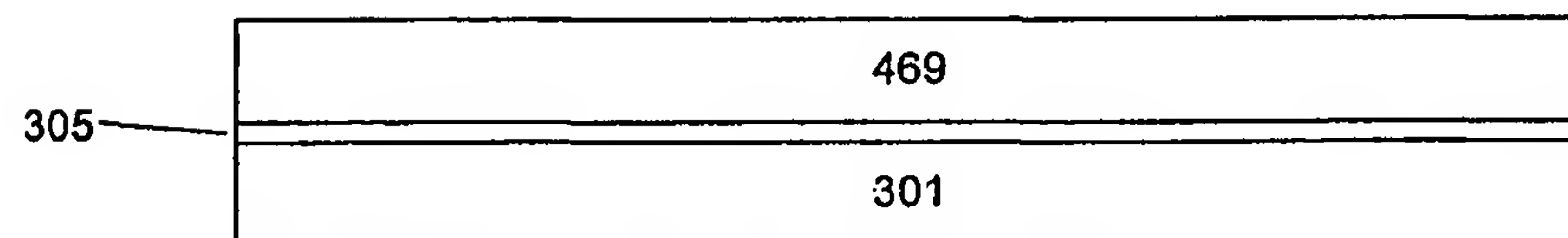


Fig. 4

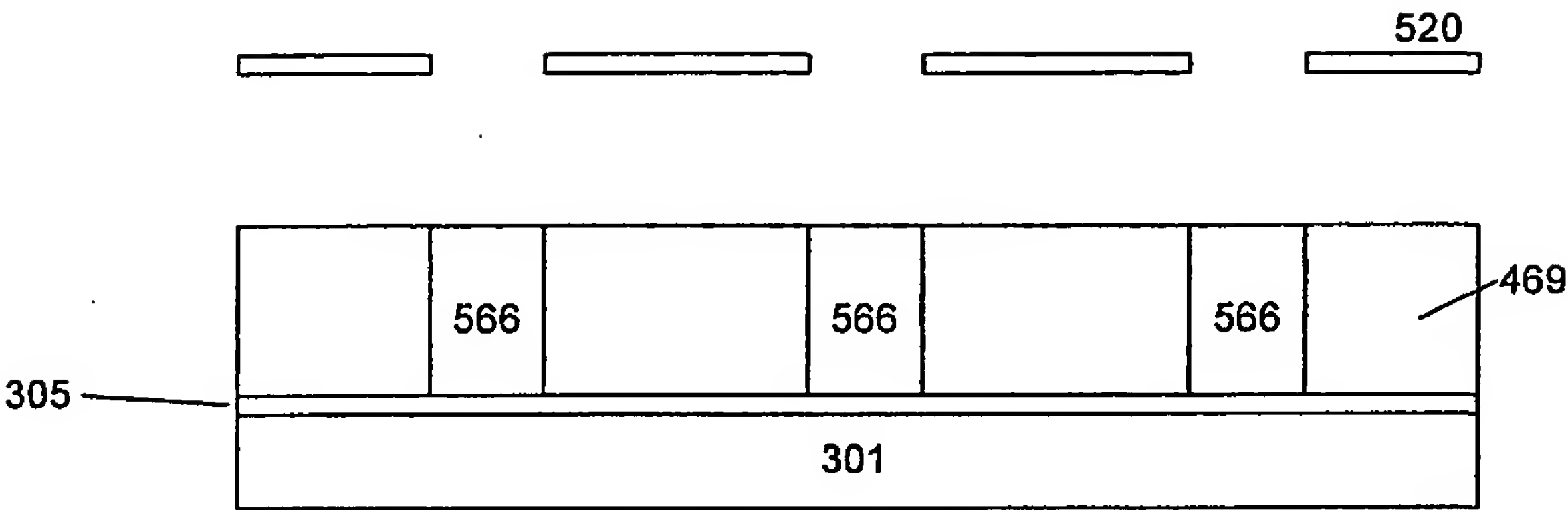


Fig. 5

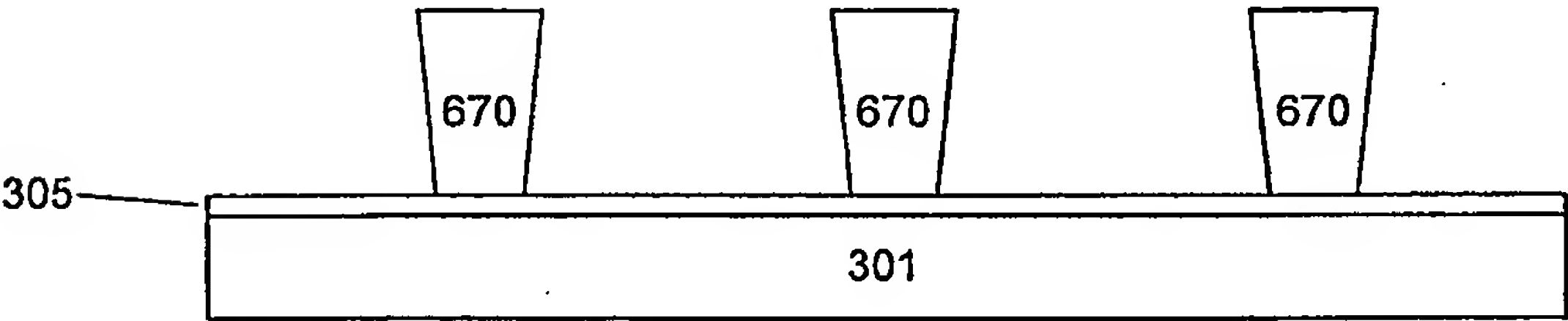


Fig. 6

5 of 5

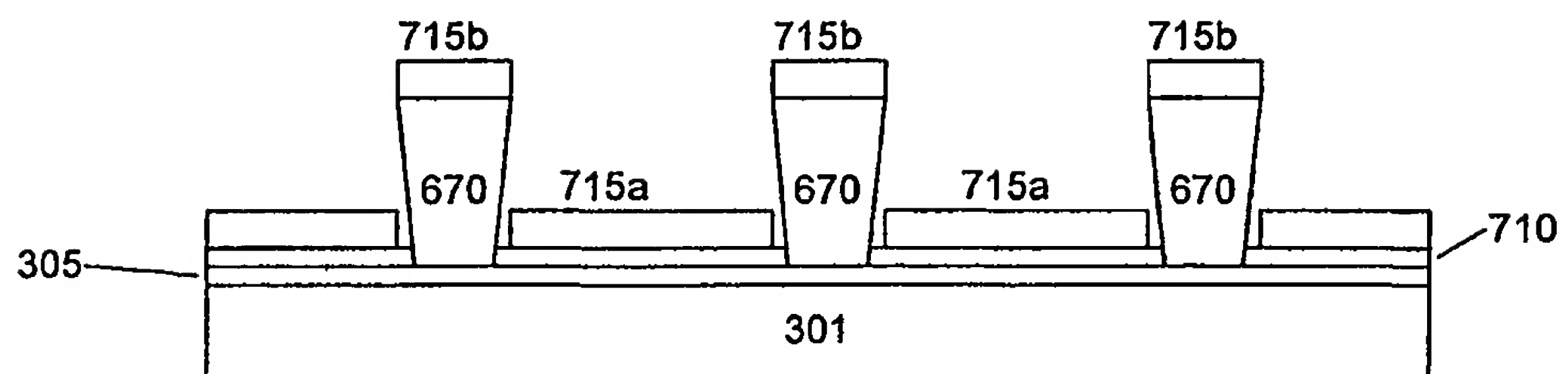


Fig. 7

INTERNATIONAL SEARCH REPORT

International application No.
PCT/SG 00/00134

CLASSIFICATION OF SUBJECT MATTER

IPC⁷: H05B 33/12

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC⁷: H05B 33/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPI, EPODOC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	DE 19918193 A1 (CAMBRIDGE DISPLAY TECHNOLOGY LTD.) 25 November 1999 (25.11.99) <i>column 9, line 67 - column 10, line 33; claims; figs. 11-13.</i>	1-16,29-30, 40-41
A	<i>the whole document.</i>	17-28,31-39, 42-62
Y	US 6111356 A (ROITMAN ET AL.) 29 August 2000 (29.08.00) <i>column 5, lines 11-67; claims; figs. 4-6.</i>	1,3-11,15-16, 29-30,40-41
A	<i>the whole document.</i>	17-28,31-39, 42-62
Y	JP 11 283752 A (FUTABA CORP) 31 January 2000 (31.01.00) (abstract). [online] [retrieved on 2001-04-27]. Retrieved from: EPOQUE PAJ Database	2,5,7-8,11-14
A	EP 0762806 A2 (MOTOROLA INC.) 12 March 1997 (12.03.97) <i>column 6, line 56 - column 7, line 49; fig. 10.</i>	1

☒ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:

„A“ document defining the general state of the art which is not
considered to be of particular relevance„E“ earlier application or patent but published on or after the international
filing date„L“ document which may throw doubts on priority claim(s) or which is
cited to establish the publication date of another citation or other
special reason (as specified)„O“ document referring to an oral disclosure, use, exhibition or other
means„P“ document published prior to the international filing date but later than
the priority date claimed„T“ later document published after the international filing date or priority
date and not in conflict with the application but cited to understand
the principle or theory underlying the invention„X“ document of particular relevance; the claimed invention cannot be
considered novel or cannot be considered to involve an inventive step
when the document is taken alone„Y“ document of particular relevance; the claimed invention cannot be
considered to involve an inventive step when the document is
combined with one or more other such documents, such combination
being obvious to a person skilled in the art

„&“ document member of the same patent family

Date of the actual completion of the international search

27 April 2001 (27.04.2001)

Date of mailing of the international search report

7 June 2001 (07.06.2001)

Name and mailing address of the ISA/AT

Austrian Patent Office

Kohlmarkt 8-10; A-1014 Vienna

Facsimile No. 1/53424/535

Authorized officer

FELLNER

Telephone No. 1/53424/345

INTERNATIONAL SEARCH REPORT

International application No.

PCT/SG 00/00134

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0762806 A2 (MOTOROLA INC.) 12 March 1997 (12.03.97) column 6, line 56 - column 7, line 49; fig. 10.	1
A	WO 99/16039 A2 (FED CORPORATION) 1 April 1999 (01.04.99) page 11, lines 4-26; figs. 8-11. -----	1

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/SG 00/00134

Patent document cited in search report			Publication date	Patent family member(s)			Publication date
DE	A1	19918193	25-11-1999	GB	A0	9808806	24-06-1998
				GB	A0	9909418	23-06-1999
				GB	A1	2336553	27-10-1999
				JP	A2	00202357	25-07-2000
EP	A2	762806	12-03-1997	JP	A2	9274452	21-10-1997
EP	A3	762806	28-10-1998	US	A	5641611	24-06-1997
JP	A2	11283752	15-10-1999			none	
US	A	6111356	29-08-2000	EP	A2	951073	20-10-1999
				EP	A3	951073	14-03-2001
				JP	A2	11329744	30-11-1999
WO	A2	9916039	01-04-1999			none	